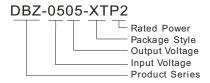


# **DBZ\_XTP2** Series

0.25W, FIXED INPUT, ISOLATED & UNREGULATED SINGLE OUTPUT

# PART NUMBER SYSTEM





## **FEATURES**

- 1500VDC isolation
- Efficiency up to 80%
- Operating temperature range: -40°C~+105°C
- Ultra-Low ripple & noise:10&20 mVp-p
- Miniature SMD package
- Internal SMD construction
- Industry standard pinout
- continuous short circuit protection

## **APPLICATIONS**

The DBZ\_XTP2 Series are designed for application where isolated output is required from a distributed power system.

These products apply to where:

- Input voltage variation ≤ ±10%;
- 2. 1.5KVDC input and output isolation;
- 3. Low ripple noise is not required.

Such as:digital circuits, low frequency analog circuits, and IGBT power device driving circuits.

SELECTION GUIDE										
	Input Voltage(VDC)	Output		Current nA)	Input Curre	ent (mA)(Typ.)	Reflected Ripple	Max. Capacitive Load(µF)	Efficiency (%, Typ.) @Max. Load	Approval
Model	Nominal (Range)	Voltage (VDC)	Max.	Min.	@Max. Load	@No Load	Current (mA,Typ.)			
DBZ-0505-XTP2	5(4.5-5.5)	5	50	5	70	15	20		80	
DBZ-1205-XTP2	12(10.8-13.2)	5	50	5	40	10	5	220	80	
DBZ-1212-XTP2	12(10.0-13.2)	12	21	2	40	10	3		80	

INPUT SPECIFICATIONS							
Item	Test Conditions	Min.	Тур.	Max.	Unit		
Input Surge Voltage	5VDC Input	-0.7		9	VDC		
(1 Sec. Max.)	12VDC Input	-0.7		18			
Input Filter		Capacitance Filter					

<b>OUTPUT SPECIFICAT</b>	IONS							
Item	Test Conditions	Test Conditions			Max.	Unit		
Output Voltage Accuracy				See tolerance envelope curve				
Line Regulation	For Vin change of±1%	For Vin change of±1%			±1.2			
Load Regulation	10% to 100% load	5V output		12	15	%		
	10% to 100% toad	12V output		7	10			
Temperature Drift	100% load				±0.03	%/°C		
Ripple & Noise*	20MHz Bandwidth			10&20		mVp-p		
Short Circuit Protection		Continuous, au	tomatic recovery	1				

Note:\* Ripple and noise tested with "parallel cable" method. See detailed operation instructions at DC-DC application notes.

<b>COMMON SPECIFI</b>	CATIONS						
Item	Test Conditions	Mi	n.	Тур.		Max.	Unit
Isolation Voltage	Input-Output, tested for 1 minute and leakage current less than 1 mA	15	00				VDC
Isolation Resistance Input-Output, test at 500VDC		10	00				ΜΩ
Isolation Capacitance	Input-Output,100KHz/0.1V	-	20				pF
Switching Frequency	100%load,nominal input	300		300	KHz		
MTBF	MIL-HDBK-217F@25℃	35	00				K hours
Case Material		Epoxy Resin (UL94-V0)		)			
Weight				1.5			g

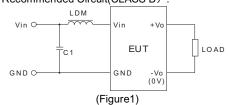


ENVIRONMENTAL SPECIFICATIONS							
Item	Test Conditions	Min.	Тур.	Max.	Unit		
Storage Humidity	Non condensing			95	%		
Operating Temperature	Power derating (above100°C)	-40		105			
Storage Temperature Coefficient		-55		125	°C		
Temp. rise at full load	Ta=25°C		15				
Soldering Temperature	1.5mm from case for 10 seconds			300			
Cooling		Free air	convection				

<b>EMC SPECIFICATIONS</b>			
ЕМІ	CE	CISPR22/EN55022 (	CLASS B(Typical Recommended Circuit to Figure1)
	RE	CISPR22/EN55022 (	CLASS B(Typical Recommended Circuit to Figure1)
EMS	ESD	IEC/EN61000-4-2	Contact ±8KV perf. Criteria B

# **EMI RECOMMENDED CIRCUIT**

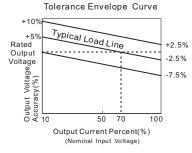
EMI Typical Recommended Circuit(CLASS B)

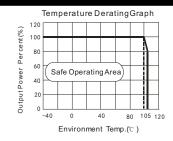


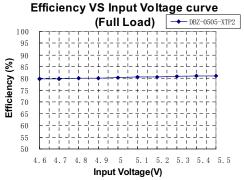
## Recommended external circuit parameters:

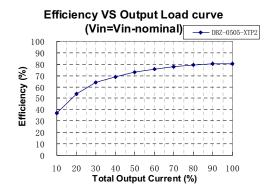
	Vin(V)	5	12		
EMI	C1	4.7µF	750V		
□IVII	LDM	6.8µH			

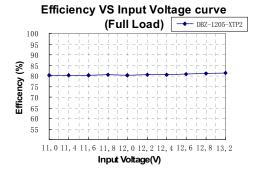
# PRODUCT TYPICAL CURVE

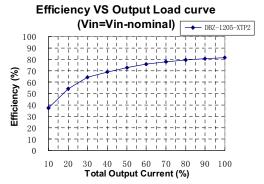








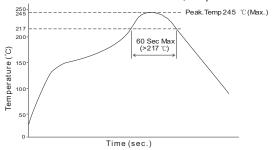




The models listed above is just for standard type. If you need the special specification product, please contact our service member by telephone presented in shortform cover or e-mail to: info@zimtec-electronics.de

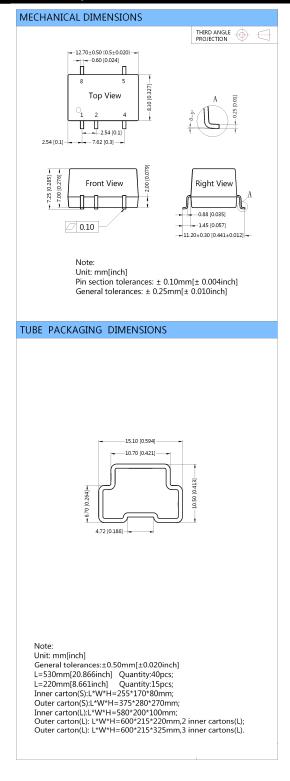


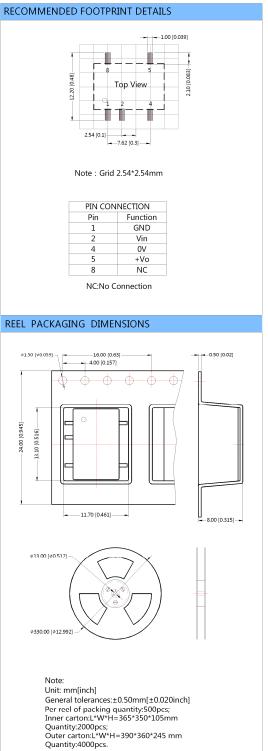
Recommended reflow soldering profile refer to IPC/JEDEC J-STD-020D standard, our products recommend reflow soldering profile as follows:



Note: The curve applies only to the hot air reflow soldering

# **OUTLINE DIMENSIONS, RECOMMENDED FOOTPRINT & PACKAGING**





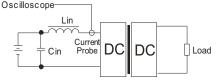
The models listed above is just for standard type. If you need the special specification product, please contact our service member by telephone presented in shortform cover or e-mail



## **TEST CONFIGURATIONS**

## Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate source impedance.



Lin(4.7μH) Cin(220μF, ESR < 1.0Ω at 100 KHz)

## DESIGN CONSIDERATIONS

## 1) Requirement on output load

To ensure this module can operate efficiently and reliably, During operation, the minimum output load **could not be less than 10% of the full load**. If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load.

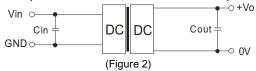
## 2) Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against overload. The simplest method is to add a circuit breaker to the circuit.

#### 3) Recommended circuit

If you want to further decrease the input/output ripple, an capacitor filtering network may be connected to the input and output ends of the DC/DC converter, see (Figure 2).

It should also be noted that the capacitance of filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the recommended capacitance of its filter capacitor sees (Table 1).

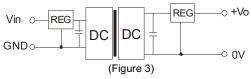


## **EXTERNAL CAPACITOR TABLE (Table 1)**

Vin	Cin	Vo	Cout
(VDC)	(µF)	(VDC)	(µF)
5	4.7	5	10
12	2.2	12	2.2

# 4) Output Voltage Regulation and Over-voltage Protection Circuit

The simplest device for output voltage regulation, over-voltage and over-current protection is a linear regulator and an capacitor filtering network with overheat protection that is connected to the input or output end in series (Figure 3), the recommended capacitance of its filter capacitor sees (Table 1), linear regulator based on the actual voltage and current to reasonable selection.



# 5) It is not recommended to increase the output power capability by connecting two or more converters in parallel. The product is not hot-swappable

## Note:

- 1. Operation under minimum load will not damage the converter; However, they may not meet all specifications.
- 2. Max. Capacitive Load is tested at nominal input voltage and full load.
- 3. Unless otherwise noted, All specifications are measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load.
- 4. In this datasheet, all test methods are based on our corporate standards.
- 5. All characteristics are for listed models, and non-standard models may perform differently. Please contact our technical support for more detail.
- 6. Please contact our technical support for any specific requirement.
- 7. Specifications of this product are subject to changes without prior notice.

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